2123

PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):J. G. Walacavage et al.

Serial No: 09/965,905 Group Art Unit: 2123

Filed: September 28, 2001 Examiner: Not Assigned

Title: METHOD OF PART FLOW MODEL FOR PROGRAMMABLE LOGIC CONTROLLER LOGICAL VERIFICATION SYSTEM

☒ **CERTIFICATE OF MAILING/TRANSMISSION (37 C.F.R. § 1.8(a)**

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STATUS LETTER

Sir:

The above-identified patent application has been on file with the United States Patent and Trademark Office for more than one year and no office actions have been received. Attorney for Applicants request the status of this application including the name and telephone number of the Examiner.

Respectfully submitted,

By:_____
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